UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 7,100,065 B2
APPLICATION NO. : 10/630182
DATED   : August 29, 2006
INVENTOR(S)   : Richard W. Adkisson

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title page, in Item (54), in column 1, in "Title", line 2, delete "SYNCHRONIZER" and insert -- SYNCHRONIZING --, therefor.

In column 1, line 2, delete "SYNCHRONIZER" and insert -- SYNCHRONIZING --, therefor.

In column 10, line 49, delete "b2c_valid ff" and insert -- b2c_valid_ff --, therefor.

In column 16, line 66, in Claim 9, delete "sync ratio" and insert -- sync_ratio --, therefor.

In column 17, line 48, in Claim 11, delete "signet" and insert -- signal --, therefor.

In column 18, line 25, in Claim 14, after "block" delete ",".

In column 18, line 65, in Claim 19, delete "b0_set" and insert -- b0_sel --, therefor.

In column 19, line 32, in Claim 22, delete "N/m$\geq$1" and insert -- N/M$\geq$1 --, therefor.

In column 20, lines 12-13, in Claim 26, delete "sync ratio" and insert -- sync_ratio --, therefor.

In column 20, line 43, in Claim 29, after "generating a" delete "skew state" and insert -- skew_state --, therefor.

In column 20, line 55, in Claim 30, delete "synch0" and insert -- syncb0 --, therefor.

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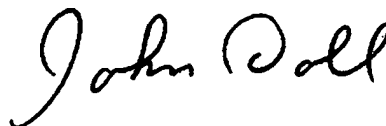INVENTOR(S)   : Richard W. Adkisson

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 20, line 57, in Claim 30, delete "signals." and insert -- signals to said core clock synchronizer controller portion. --, therefor.

Signed and Sealed this

Seventeenth Day of February, 2009

JOHN DOLL
Acting Director of the United States Patent and Trademark Office